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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/673,721	09/29/2003	Jeffrey A. Hall	03-0964	2466
24319	7590	09/06/2005		
LSI LOGIC CORPORATION 1621 BARBER LANE MS: D-106 MILPITAS, CA 95035			EXAMINER DO, THUAN V	
			ART UNIT	PAPER NUMBER
			2825	

DATE MAILED: 09/06/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	<b>Application No.</b>	<b>Applicant(s)</b>	
	10/673,721	HALL ET AL.	
	<b>Examiner</b>	<b>Art Unit</b>	
	Thuan Do	2825	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

#### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

- 1) ☒ Responsive to communication(s) filed on 29 September 2003.
- 2a) ☐ This action is FINAL.                      2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-4, 7-10, 13-16, 19 and 20 is/are rejected.
- 7) ☒ Claim(s) 5, 6, 11, 12, 17 and 18 is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 9/29/03 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All    b) ☐ Some \*    c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

- |   |   |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)             | 4) <input type="checkbox"/> Interview Summary (PTO-413)                     |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)    | Paper No(s)/Mail Date. _____  |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| Paper No(s)/Mail Date _____   | 6) <input type="checkbox"/> Other: _____                                    |

## **DETAILED ACTION**

1. This office action is responsive to application filed on 09/29/2003. Claims 1-20 are pending in this office action.

### ***Claim objections***

Claim 1, the terms:

“standardized functional blocks”,  
“according to functions desired”,  
“associated with the desired ones”,

What are their definitions or what applicants intend to mean. Clarification or correction is required.

### ***Claim Rejections - 35 USC § 102***

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –  
(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1-4,7-10,13-16,19,20 are rejected under 35 U.S.C. 102(b) as being unpatentable over Vinciarelli et al., Pat. No. 6110213.

**Regarding claim 1:** Vinciarelli teaches a method comprising:

designing the integrated circuit with a plurality of integrated circuit standardized functional blocks, where each of the plurality of integrated circuit standardized functional blocks has a known function and a known integrated circuit contact array pattern, and the integrated circuit is designed by selecting desired ones of the integrated circuit

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standardized functional blocks according to functions desired for the integrated circuit (col. 1, lines 50-62, col. 4, lines 25-30) , and

designing the package substrate with a plurality of package substrate standardized functional blocks, where each of the plurality of package substrate standardized functional blocks has a known package substrate contact array pattern, a known signal trace routing layer pattern, a known ground plane layer pattern, and a known power plane layer pattern (col. 1, lines 50-62),

where a given one of each of the plurality of package substrate standardized functional blocks is associated with a given one of the plurality of integrated circuit standardized functional blocks, and the package substrate is designed by selecting package substrate, rate standardized functional blocks associated with the desired ones of the integrated circuit standardized functional blocks (col. 1, lines 50-62, col. 4, lines 25-30).

**Regarding claim2-4:** Vinciarelli teaches a method with layers (col. 1, lines 50-62).

The remaining claims of 102(b) section contain features similar to the rejection of claims 1-4 and rejected in the rationale.

### ***Allowable Subject Matter***

Claims 5,6,11,12,17,18 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims. The reason for allowance is that the prior art of record does not teach the dependent claims with all other features of corresponding independent claims.

### **CONTACT INFORMATION**

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thuan Do whose telephone number is 571-272-1891. The examiner can normally be reached on Monday-Friday 8:30-4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew S. Smith can be reached on 571-272-1907. The fax phone numbers for the organization where this application or proceeding is assigned are 571 273-8300 for communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0596.

A handwritten signature in black ink, appearing to read 'Thuan Do', with a stylized, cursive script.

Thuan Do  
Primary examiner  
09/01/2005